Your best embedded solution partner.

No. 1 company in JTAG ICE for embedded systems.
Always with the most advanced technology.

Our aim is to develop unique, reliable and usable products especially in embedded development, not to be a mall of development tools. We are going to be a company coexisting with users, semiconductor manufacturers, and third party developers.
Kyoto Microcomputer Co., Ltd.

- President and CEO: Syoichi Yamamoto
- Founded in 1985 (The 20th anniversary in 2005)
- Headquarters: Kyoto JAPAN
  - Tokyo branch
- URL: http://www.kmckk.co.jp/eng/
- E-mail: en-info@kmckk.co.jp
- Employees: 12 (Mar. 2005)
- Primary field: Development of embedded development tools including JTAG emulators.
- We have 20 years experience in development of debugging tools.
# Management Team

<table>
<thead>
<tr>
<th>Role</th>
<th>Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>President and CEO</td>
<td>Syoichi Yamamoto</td>
</tr>
<tr>
<td>Director of R&amp;D and CTO</td>
<td>Isao Tsukagoe</td>
</tr>
<tr>
<td>Director of Sales</td>
<td>Shigeru Morioka</td>
</tr>
<tr>
<td>Director of Administration</td>
<td>Seiji Yamamoto</td>
</tr>
<tr>
<td>G.M. of Tokyo Office</td>
<td>Kunihiko Tsuji</td>
</tr>
<tr>
<td>Director of Strategic Marketing</td>
<td>Shoji Ueda</td>
</tr>
</tbody>
</table>
KMC’s ecosystem

Developers

Partner for Embedded developer
Advanced features at affordable price
Cutting-edge technology
Extensive user support

Partnership with CPU vendors
Adopts new device technology
Suggests new debug features

Partnership with 3rd parties
KMC’s debug technology and APIs
Custom development environment

ARM
SONY NEC
Panasonic
KMC
MIPS Technologies
TOSHIBA
MONTAVISTA
symbian
Midac lab
CE Linux Forum

CPU vendors
3rd parties
History of our company  1985-1992

• Founded in 1985
  • PROASM-II  General macro cross assembler
  • Turbo-V   V30 accelerator card for NEC PC-98 series
• 1986
  • Turbo-V/180/Σ
  • Turbo-180   64180 daughter CPU card for CP/M emulation and cross development
• 1988
  • PARTNER v. 2.0 runs on NEC PC-98 series
  • PARTNER-T
  • Multi-CPU supported target debugger
• 1990
  • PARTNER v. 3.0 runs on MS-DOS
  • EXE386/286   DOS extender, software to extend MS-DOS memory space
• 1991
  • Turbo-486   80486 CPU accelerator card for PC-98
• 1992
  • PARTNER-ET   KMC’s first ROM in-circuit emulator
History of our company 1995-1999

- 1995
  - PARTNER/Win  Source level debugger software on Windows 95
  - exeGCC   GNU C (GCC) compiler for RISC CPU embedded system development
- 1996
  - PARTNER-N64   Development Environment for NINTENDO N64
  - Evaluation boards for V821 and SH1
- 1997
  - PARTNER-ET II   KMC’s 3rd generation ROM in-circuit emulator
  - Evaluation boards for SH3 and VR4300
- 1998
  - JTAG support for PARTNER-ET II
  - JTAG emulator PARTNER-J
  - Evaluation boards for M32R/MN103/V832/SH4/S3HDSP
- 1999
  - Development Starter Kit for SH4 and V832   All in one development environment
  - Support C++ for primary KMC products
2000
• Moved to new company building
• Solution Platform for reducing development cycle

2001
• PARTNER-Tiny
• Solution Platform for ARM7

2002
• Solution Platform for VR4131 / ARM9 / SH4R

2003
• Opened Tokyo branch
• Super high-speed JTAG ICE PARTNER-Jet
• Solution Platform for V850E

2004
• JTAG ICE PARTNER-JetM supported ROM emulation memory unit
• PARTNER-eLinux Embedded Linux debugging environment
  • Panasonic Mobile used PARTNER-eLinux to develop FOMA P901i cellular phone for NTT DoCoMo

2005
• Eclipse for PARTNER DevKit (TBA)
• Eclipse and PARTNER operate together
KMC’s Primary Products

**Solution Platform**

**Innovative JTAG**

**Super high-speed JTAG ICE**

**One JTAG ICE fits all**

**Debug Innovation**

JTAG ICE with ROM emulation memory unit

**PARTNER-JetM**

- PARTNER-JetM / ARM
- PARTNER-JetM / MIPS
- PARTNER-JetM / SH
- PARTNER-JetM / VB50
- PARTNER-JetM / MN103

**PARTNER-Jet**

Innovative JTAG TARGET DEBUGGER

- PARTNER-Jet / ARM
- PARTNER-Jet / MIPS
- PARTNER-Jet / SH
- PARTNER-Jet / VB50
- PARTNER-Jet / MN103
Future Challenge

Aim No.1 in the world

Thank you!