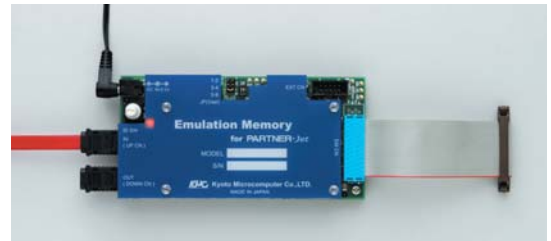


Linux	Windows CE	T-Engine
SMP-Linux	Multicore CPU	Eclipse
Visual Studio 2005 for Windows CE	PARTNER	Event Tracker

High-speed data download JTAG: 3MByte/sec
 USB2.0/1.1 support, LAN(100Base-TX/10Base-T) support (Model 30/40)
 Huge built-in trace memory **Max.8GBit**
 High-speed trace clock support **400MHz²**
 Expand CPU support (Model 20/30/40)

By purchasing optional debugger soft (included JTAG Probe) for additional CPU group, user can debug other CPU groups. For example, a user purchased PARTNER-Jet/SH and then purchase the option debugger soft for ARM, the user is able to use the full function as PARTNER-Jet/ARM as same as PARTNER-Jet/SH instead to purchase the whole JTAG Emulator.

Multi-core CPU support (Option)
 Option, ROM Emulation support (Model20/30/40)
 VLINK support : 1MByte/sec*1
 Various OS support; Linux,T-Engine,Windows CE and more (Option)
 SMP-Linux support (Option)
 Integrated Developing Environment Eclipse support
 FLASH memory writing support (NOR Flash)
 Debugger software, PARTNER, is included



Option Emulation Memory

Supported CPU

ARM	MIPS	SH	V850	MN103
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	PARTNER-Jet Model10	PARTNER-Jet Model20	PARTNER-Jet Model30	PARTNER-Jet Model40
JTAG Clock	Max.66MHz (Auto setting)			
Software Break	63 x 256 points			
Hardware Break	Using CPU embedded Hardware Break			
Real-time Trace	—	Over 200MHz Trace capability 4.5Mbit	Over 200MHz Trace capability 18Mbit	Over 400MHz(ARM CPU) ² Trace capability 8Gbit
Host Interface	USB 2.0/1.1	USB 2.0/1.1	USB 2.0/1.1, LAN 100BASE-TX/10BASE-T	USB 2.0/1.1, LAN 100BASE-TX/10BASE-T
Expanded interface	—	400Mbps Serial Interface	400Mbps Serial Interface	600Mbps Serial Interface
Power Consumption	500mA/8.5V			
Size	109(W) x 148(D) x 51(H)mm			
Operation Environment	Temperature: Between 5 - 35 (41 - 95) Humidity: 0 - 85% (Non Condensing)			

¹ The speed is depends on the target and CPU conditions

² In case of the trace clock is 200MHz with dual edge in ARM CPU. The actual capable speed may be limited under the condition of CPU and target

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PARTNER Creates New Debug Environment

Kyoto Microcomputer Co., Ltd.